

IN THE CLAIMS:

Please cancel claims 1 and 7-8 without prejudice or disclaimer, amend claims 2 and 5, and add claim 10 as follows:

1. (Cancelled)
2. (Currently Amended) A display device comprising:
signal lines which are formed on an upper surface side of a substrate to provide a display region;
an insulation film which is formed such that the film covers the signal lines except for terminal portions of the signal lines in periphery of the substrate; and
conductive layers which extend in ~~[[the]]~~ an extension direction of the signal lines such that the conductive layers traverse the terminal portions, wherein
a pair of gaps are formed between on respective sides of each of the conductive layers parallel to the extension direction of the conductive layer as well as between said each conductive layer and the insulation film, and the insulation film and
a pair of holes are formed in the signal lines at portions underneath and corresponding to the pair of gaps along the extension direction of the signal lines, and
said each conductive layer is formed on the signal lines and between the pair of holes, while the insulated film is formed on the signal lines and outside of the pair of holes.
3. (Original) A display device according to claim 2, wherein the display region includes gate signal lines and drain signal lines, wherein a material of the signal lines is equal to a material of the gate signal lines, and a material of the conductive layers is equal to a material of the drain signal lines.
4. (Original) A display device according to claim 2, wherein gate signal lines, drain signal lines and interlayer insulation films which are formed between the respective signal lines are formed on a display region, and a material of the insulation films is identical with a material of the interlayer insulation film.

5. (Currently Amended) A display device comprising:
signal lines which are formed on an upper surface side of the substrate to provide a display region;

semiconductor layers which are formed below the signal lines by way of a first insulation film such that the semiconductor layers traverse the signal lines at terminal portions of the signal lines in a periphery of the substrate;

a second insulation film which is formed on top of the substrate ~~such that the second insulation film also~~ to cover[[s]] the signal lines and in which holes are formed [[at]] above regions thereof where the semiconductor layers are formed; [[and]]

conductive layers which have respective sides thereof in the extension direction of the signal lines arranged at both sides of the signal lines and are connected with respective semiconductor layers,

wherein the display region includes thin film transistors, and
a material of the semiconductor layers in the periphery of the substrate is equal to a material of semiconductor layers of the thin film transistors in the display region ~~the resistance of the semiconductor layers is lowered by introducing impurities there into using the signal lines as masks.~~

6. (Original) A display device according to claim 5, wherein the display region includes gate signal lines and drain signal lines, wherein a material of the signal lines is equal to a material of the gate signal lines, and a material of the conductive layers is equal to a material of the drain signal lines.

7-8. (Cancelled)

9. (Original) A display device according to claim 5, wherein gate signal lines, drain signal lines and interlayer insulation films which are formed between the respective signal lines are formed on a display region and a material of the second insulation film is identical with a material of the interlayer insulation films.

10. (New) A display device comprising:
signal lines which are formed on an upper surface side of a substrate to provide a display region;

an insulation film which is formed to cover the signal lines except for terminal portions of the signal lines in periphery of the substrate; and

conductive layers which extend in an extension direction of the signal lines such that the conductive layers traverse the terminal portions,

wherein each of the signal lines branches to three along the extended direction to provide a central portion and two side portions one two sides of the main portion, and

said each conductive layer is formed on the central portion, and the insulation film is formed on the side portions.